

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Manoj Mehrotra, et al.

Docket No: TI-32513.1

Serial No: Not Assigned

Conf. No: Not Assigned

Examiner: TBD

Art Unit: TBD

Filed: 10/22/2003

For: TRANSISTOR WITH BOTTOMWALL/SIDEWALL JUNCTION CAPACITANCE
REDUCTION REGION AND METHOD

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Commissioner For Patents
P.O. Box 1450
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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO/SB/08A. A copy of the noted references were provided in connection with the parent application and are therefore not being supplied herewith.

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Respectfully submitted,



Peter K. McLarty
Attorney for Applicants
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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known	
				Application Number	
				Filing Date	10/22/2003
				First Named Inventor	Manoj Mehrotra, et al.,
				Group Art Unit	
Examiner Name					
Sheet	1	of	1	Attorney Docket No.	TI-32513.1

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	6,093,610		Rodder	07/25/2000	
	AB	5,976,937		Rodder, et al.	11/02/1999	
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	AH					
	AI					
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FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.